

## Interview with CEO Coby Hanoch

### Extending the successful Leti collaboration

WBT recently announced it has extended its successful collaboration with French research institute Leti in order to expedite the commercialisation of its SiOx ReRam technology. Essentially, WBT will be fine-tuning and optimizing cell parameters ahead of potential commercial deals.

#### Further narrowing the parameters

While cell parameters, such as endurance and data retention, are already being optimized during the current scale up to 1Mb, we anticipate WBT will need to perform base line improvements to further narrow the parameters to optimize the technology ahead of future license deals, i.e. to get SiOx ReRam cells production ready. Additionally, initial work is likely to be done to prepare the technology for eventual production in different fab types.

We would expect this process may take another six to nine months of development work, i.e. into the first half of 2019.

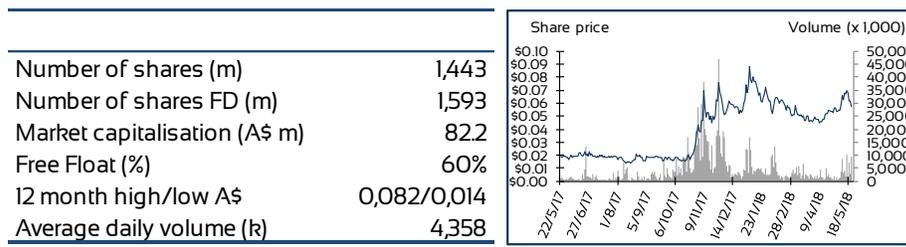
CEO COBY HANOCH TALKS US THROUGH RECENT DEVELOPMENTS



Coby Hanoch  
CEO of Weebit Nano Limited

#### Achieved 4Kb array at 40nm resolution

In February 2018 WBT announced it had achieved 4kb arrays (4,096 memory cells) at a resolution of 40nm with 99% working cells, thereby further underlining the company's development strength.



## Weebit Nano Ltd. (ASX:WBT)

Semiconductors &  
Semiconductor Equipment

Australia

Risk: High

Founded in Israel in 2015, Weebit Nano Ltd (WBT) is developing a newly emerging computer memory technology that combines the best of today's mainstream memory technologies, i.e. DRAM and Flash memory. Non-volatile like Flash and nearly as fast as DRAM, WBT's SiOx ReRAM will likely be able to complement and partially replace DRAM and Flash if and when the technology can be commercialized in a few years' time.

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## BUY

Current price: A\$ 0.057

Price target: A\$ 0.07

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### 1Mb array expected by mid-2018

WBT's next target is to achieve a 1Mb array of memory cells (1,024,000 individual memory cells) by the middle of 2018. Constructing a 1Mb brings a lot of complexity given the cell-to-cell interaction/interference that takes place when addressing memory cells deep inside the chip. However, once completed the company will have achieved a very significant milestone, given that most embedded memories today are 1MB in size or less.

*In other words, WBT will have achieved the key memory size at the key resolution, i.e. 1MB at 40nm, to address its main market of embedded memory and System on a Chip (SoC).*

### Move towards 28nm resolution

While we believe demonstration of working Mb arrays will be sufficient for WBT to start addressing its initial target markets, i.e. embedded memory and SOC, the company indicated it is now planning for memory integration at more advanced resolutions as well.

This technology node is likely to be 28nm initially but could possibly be extended further towards 22nm in due time, in our view. Achievement of working cells at a 2X resolution would open up a range of high-end embedded memory applications but we believe this should be considered a secondary objective at this time. The first priority is to land industry partners who want to take the 40nm node into production and are willing to spend money up front, i.e. in Non-Recurring Engineering (NRE) payments to WBT and to fund transition of the technology into their own fabs in due time.

### Two heavy hitters appointed to the board

Recently, WBT appointed two new impressive members to its board, i.e. Executive Director Yaov Nissan-Cohen and Non-Executive Director Fred Bart.

We are particularly impressed with Mr. Nissan-Cohen's appointment given his background in the NVM space. Furthermore, we believe his tenure at foundry Tower Semiconductor, currently TowerJazz (NASDAQ:TSEM) bodes well for potential future deals within the foundry space in general.

Mr. Nissan-Cohen studied with Dov Frohman who invented erasable programmable read only memory (EPROM) in 1970. EPROM was the first non-volatile memory (NVM) and sat between RAM (Random Access Memory), which is volatile, and ROM (Read Only Memory), which is non-volatile but with fixed values once the memory was programmed. EPROM's provided a non-volatile and reprogrammable type of memory and ultimately led to Flash memory.

Mr. Nissan-Cohen was also one of the founders of Saifun Semiconductors, a provider of intellectual property (IP) solutions for the non-volatile memory market, which was acquired by Spansion for US\$ 368M in 2008. As such, he has extensive experience with the IP licensing business model that WBT is most likely to use in its commercialization process. Mr. Nissan-Cohen will be working with WBT on a part time basis on both technical and business matters.

Given Mr. Bart's extensive experience with the development of MEMS technology at Audio Pixels (ASX:AKP), of which he is Chairman and CEO, we believe WBT also has substantially expanded its in-house experience with regards to running an ASX-listed Technology company.

*Combined with the Intel pedigree of Chairman David Perlmutter, we believe WBT has essentially built a board of semiconductor rock stars.*

#### Buy rating reiterated and A\$ 0.07 price target unchanged

With the new additions to the board, the renewed collaboration with Leti and the anticipated achievement of a 1MB array or SiOx ReRam memory cells in the very near term, we believe the company has set itself up for a well-planned transition from being a development company to an IP licensing company.

A major next step would be to land a first strategic partner/customer.

We reiterate our Buy recommendation for WBT and leave our price target of A\$ 0.07 per share unchanged.



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